

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Vitaly LAGOON et al

Serial No.: 09/939,743

Filed: August 28, 2001

§ Group Art Unit: 2133

For: Method for Providing Bitwise Constraints
for Test Generation

§ Attorney
§ Docket: 24771
§ Previously V02/14

Examiner: Dipakkumar B. Gandhi

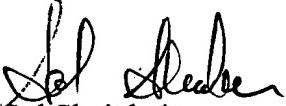
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

FORMAL DRAWINGS

Sir:

We enclose herewith a new set of formal drawings (1 sheet) for the above-identified patent application.

Respectfully submitted,


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Registration No. 25,457

Date: September 1, 2004